

CrystaLatch™ 1x1, 1x2 Series Fiber Optic Switch

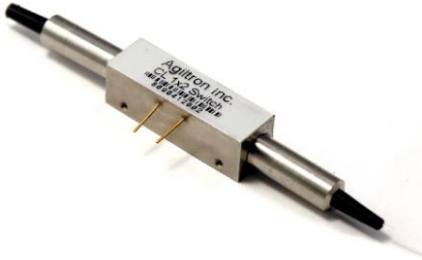
(SM, PM, Unidirectional, Bidirectional, High Power)



(Protected by U.S. patents 7224860, 6757101, 6577430 and pending patents)

DATASHEET

BUY NOW



Features

- Solid-State high speed
- Ultra-high reliability
- Fail-safe latching
- Low insertion loss
- Direct low voltage drive
- Compact
- Low cost

Applications

- Optical channel blocking
- Configurable Add/Drop
- System monitoring
- Instrumentation

The CL 1x1, 1x2, 2x1 Series Fiber Optical Switch redirects an incoming optical signal into a selected output fiber, achieved using patented non-mechanical configurations and activated via an electrical control signal. The latching operation preserves the selected optical path after the drive signal has been removed. The all solid state CL 1x1,1x2 fiber optic switch features low insertion loss, high extinction ratio, high channel isolation, and extremely high reliability and repeatability. It is designed to meet the most demanding switching requirements of continuous operation without failure, over 25 years of longevity, operation in shock/vibration environments and large temperature variations, and fast response time.

The switch also has build-in circulator and isolator functions. Electronic driver is available for this series of switches.

The magneto-optical crystals used in the CL switches have no fatigue nor drift effect.

Specifications

| Parameter | Min | Typical | Max | Unit |
|---------------------------------------|------------------|---------|---------------------------|--------|
| Operation Wavelength ^[1] | 1520 | 1550 | 1580 | nm |
| | 1295 | 1310 | 1325 | |
| Insertion Loss ^[2] | | 0.7 | 1.0 (1.2 ^[4]) | dB |
| Cross Talk | Bidirectional | 35 | 50 | dB |
| | Unidirectional | 40 | 50 | dB |
| Return Loss | 50 | 55 | | dB |
| PDL (SM Series) | | 0.1 | 0.2 | dB |
| Extinction Ratio (PM Series) | 18 | 25 | | dB |
| Optical Switching Speed (Rise, Fall) | 5 | | 10 | μs |
| Repetition Rate | | 2K | | Hz |
| Polarization Mode Dispersion | | 0.1 | 0.2 | ps |
| Operating Temperature | -5 | | 70 | °C |
| Storage Temperature | -40 | | 85 | °C |
| Optical Power Handling ^[3] | | 300 | 500 | mW |
| | | | 2 | W |
| Durability | 10 ¹⁵ | | | cycles |

Notes:

[1]. Agiltron can achieve same SPEC at L band

[2]. Measured without connectors

[3]. Special operating temperature -40 to +85°C is available with Ordering Information

[4]. For special operating temperature, lower than -20°C and higher than +70°C

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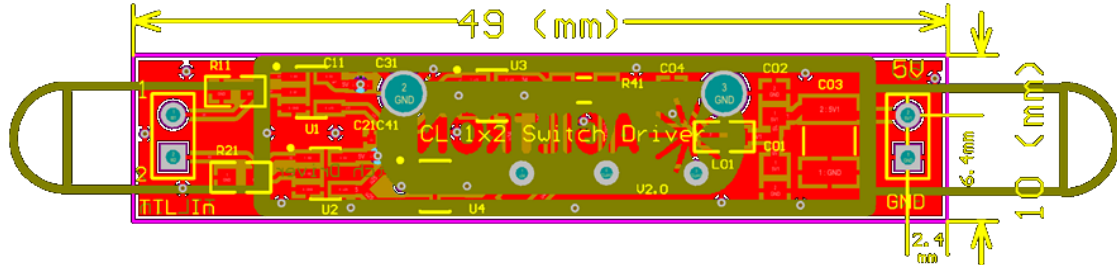


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Electrical Driver

TTL Fast Driver Part Number DV CL1x2/2x2 TTL



1. Connect GND Pin to ground
2. Apply 5V DC power to 5V Pin
3. Input 0-5V pulse to Pin 1 and Pin 2. One polarity is one state, reverse the polarity switch to the other state
4. position. Applying a reversed polarity pulse, another light path will be connected and latched to the position after pulse removed.

Electrical Driving Information

The switch is actuated by applying a voltage pulse. Applying one polarity pulse, one light path will be connected and latched to the position. Applying a reversed polarity pulse, another light path will be connected and latched to the position after pulse removed.

| Parameter | Minimum | Typical | Maximum | Unit |
|-----------------------------|---------|---------|---------|------|
| Drive Voltage | 4.5 | 5 | 5.5 | V |
| Resistance (each Pin Group) | 15 | 18 | 22 | Ω |
| Pulse Duration | 0.2 | 0.3 | 0.5 | ms |

Driving kit with USB and TTL interfaces and Windows™ GUI is available. We also offer RS232 interface as an option – please contact Agiltron sales.

Bidirectional Series 1x1, 1x2 or 2x1 Switch Driving Table

| Optical Path | | Pin 1 | Pin 2 |
|-----------------|-----------------|-------|-------|
| 1x1 | 1x2 or 2x1 | | |
| Port 1 ↔ Port 2 | Port 1 ↔ Port 2 | 0 | + |
| Dark | Port 1 ↔ Port 3 | + | 0 |

“+” is 4.5 ~ 5.5 V pulse, typical pulse is 5 V

Unidirectional Series 1x1, 1x2 Switch Driving Table

| Optical Path | | Pin 1 | Pin 2 |
|-----------------|-----------------|-------|-------|
| 1x1 | 1x2 | | |
| Port 1 → Port 2 | Port 1 → Port 2 | 0 | + |
| Dark | Port 1 → Port 3 | + | 0 |

“+” is 4.5 ~ 5.5 V pulse, typical pulse is 5 V

Unidirectional Series 1x1, 2x1 Switch Driving Table

| Optical Path | | Pin 1 | Pin 2 |
|-----------------|-----------------|-------|-------|
| 1x1 | 2x1 | | |
| Port 2 → Port 1 | Port 2 → Port 1 | + | 0 |
| Dark | Port 3 → Port 1 | 0 | + |

“+” is 4.5 ~ 5.5 V pulse, typical pulse is 5 V

Note:

The driving voltage value is transient voltage with a full load. The driver circuitry needs to provide sufficient current (~300mA) during the switching. Inside the switch core is an electromagnet with a residual magnetic field. The residual magnetic field will be established when an electrical current flows in one direction through the coil for a sufficiently long period. The residual magnetic field latches the switch state even without applying a voltage (the current flow stopped). Flowing a current in the opposite direction for a sufficient time changes the switch stage by establishing a reversal magnetic field. The coil is forgiving to the driver unless one burns it by applying a higher voltage or a current for too long (day). The switch can also be operated at high repetition rates of kHz, where the residual magnetic field may not be fully established.

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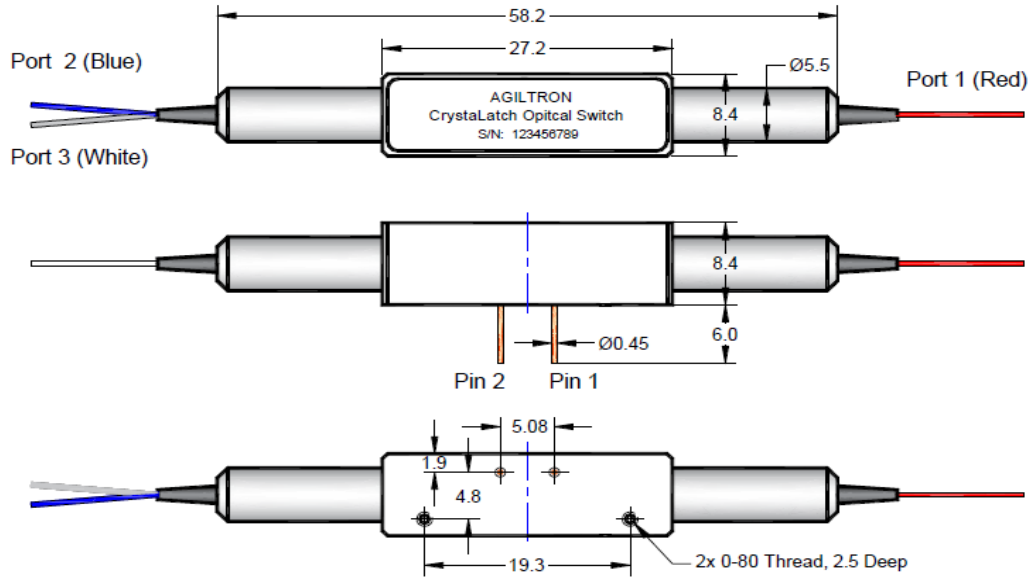
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Mechanical Dimensions (Unit: mm)



*Product dimensions may change without notice. This is sometimes required for non-standard specifications.

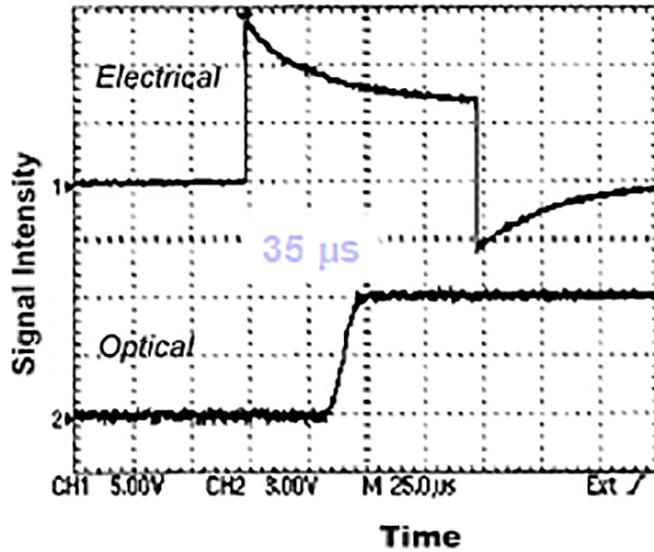
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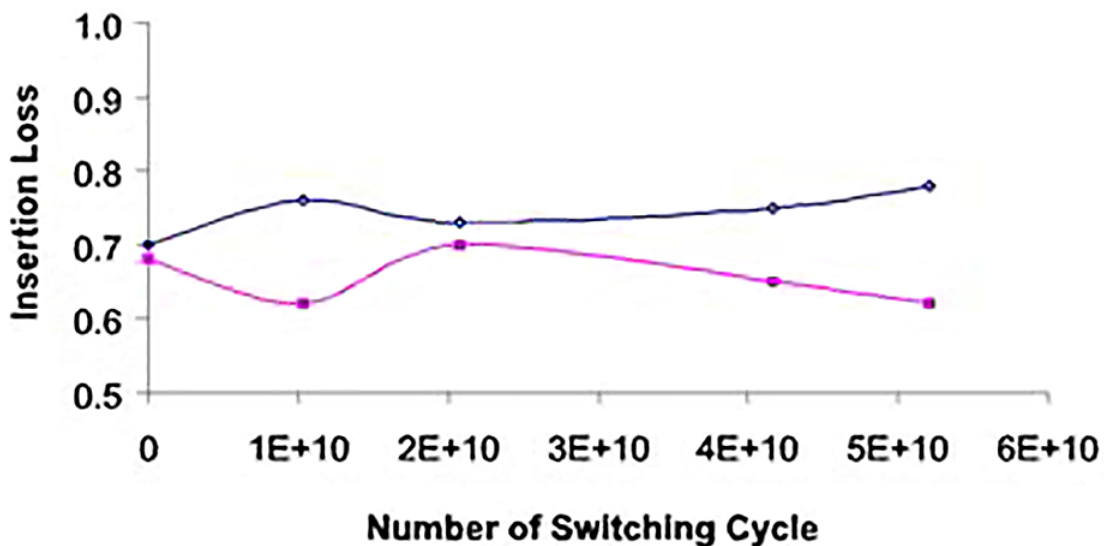
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Typical Switching Response



Typical Loss Change of 1x2 vs Switching Numbers



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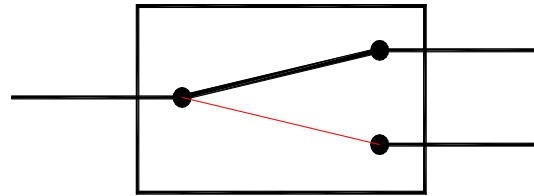
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Functional Diagram



CL 1x2 Series Switch

Ordering Information

| Prefix | Type | Wavelength | Switch | Package | Fiber Type | Fiber Cover | Fiber Length | Connector ^[5] |
|------------------------------|--------------|-------------|-------------------------------|---------------|-------------|-----------------|--------------|--------------------------|
| CLSW - ^[1] | 1x1 = 11 | 1310 = 3 | Dual Stage ^[6] = 2 | Standard = 3 | SMF-28 = 1 | Bare fiber = 1 | 0.25m = 1 | None = 1 |
| CLPM - ^[2] | 1x2 = 12 | 1550 = 5 | Single Stage = 1 | -40~+85°C = A | PM 1550 = B | 900 um tube = 3 | 0.5m = 2 | FC/PC = 2 |
| CLBD - ^[3] | 2x1 = 21 | Special = 0 | | -40~+70°C = B | PM 1310 = D | Special = 0 | 1.0m = 3 | FC/APC = 3 |
| CLPB - ^[4] | Special = 00 | | | -20~+85°C = C | Special = 0 | | Special = 0 | SC/PC = 4 |
| | | | | -20~+70°C = D | | | | SC/APC = 5 |
| | | | | Special = 0 | | | | ST/PC = 6 |
| | | | | | | | | LC/PC = 7 |
| | | | | | | | | Duplex LC/PC = 8 |
| | | | | | | | | LC/APC = A |
| | | | | | | | | LC/UPC = U |
| | | | | | | | | Special = 0 |

- [1]. **CLSW**: CrystaLatch 1x1, 1x2 SM **SWITCH**.
- [2]. **CLPM**: CrystaLatch 1x1, 1x2 **PM** Switch.
- [3]. **CLBD**: CrystaLatch 1x1, 1x2 SM **BIDIRECTIONAL** Switch.
- [4]. **CLPB**: CrystaLatch 1x1, 1x2 **PM Bidirectional** Switch.
- [5]. Contact us for high power connectors.
- [6]. Using two switching cores for high on/off ratio

Fiber Core Alignment

Note that the minimum attenuation for these devices depends on excellent core-to-core alignment when the connectors are mated. This is crucial for shorter wavelengths with smaller fiber core diameters that can increase the loss of many decibels above the specification if they are not perfectly aligned. Different vendors' connectors may not mate well with each other, especially for angled APC.

Fiber Cleanliness

Fibers with smaller core diameters (<5 μm) must be kept extremely clean, contamination at fiber-fiber interfaces, combined with the high optical power density, can lead to significant optical damage. This type of damage usually requires re-polishing or replacement of the connector.

Maximum Optical Input Power

Due to their small fiber core diameters for short wavelength and high photon energies, the damage thresholds for device is substantially reduced than the common 1550nm fiber. To avoid damage to the exposed fiber end faces and internal components, the optical input power should never exceed 20 mW for wavelengths shorter 650nm. We produce a special version to increase the handling by expanding the core side at the fiber ends.

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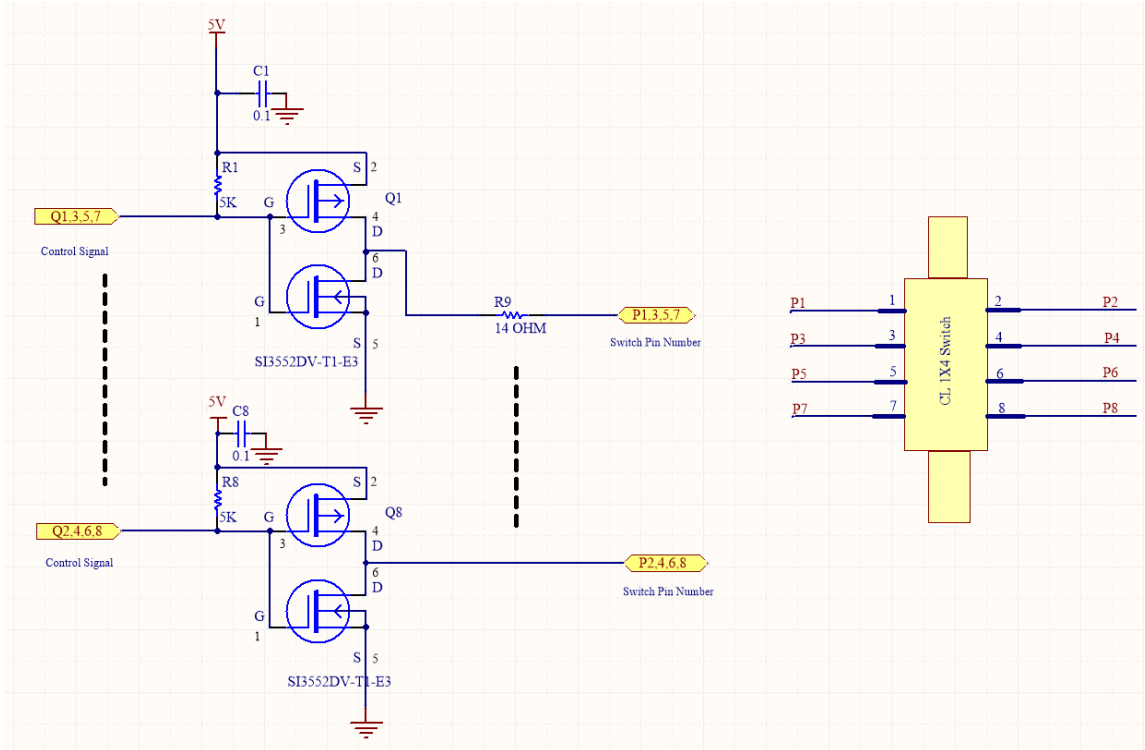


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Driver Design Example for 1x4

A recommended +5VDC powered driving circuit is provided. The resistor network R1~R8 is to suppress the driving signal's voltage level to meet the "switch voltage" requirements. In specific applications, users can use lower voltage to eliminate the R1~R8. The Q1~Q8 is the control signal from either a function generator or a microcontroller general purpose I/O. The Q1~Q8 switching speed must meet the specific MOSFET switching requirement and CL 1x4 Switch specific requirement. Usually, the control signal speed is $\leq 2\text{kHz}$.



Usually, a clean power supply source will be sufficient. However, decoupling capacitors for the transistor supply rail are recommended depending on different applications. Minimum the current loop on the switching circuits will minimize the switching noise. For other layout recommendations, please refer to books or application notes from the IC manufacturer.